

Amendments to the Specification:

Please amend the Title as follows:

MEMORY CONTROLLER WITH 1X/MX WRITE CAPABILITY DRIVER CIRCUITRY
HAVING A MULTIPLEXING STAGE TO PROVIDE DATA TO AT LEAST N-1 OF N
DATA PROPAGATION CIRCUITS, AND HAVING OUTPUT MERGING CIRCUITRY
TO ALTERNATELY COUPLE THE N DATA PROPAGATION CIRCUITS TO A DATA
PAD TO GENERATE EITHER A 1X OR MX STREAM OF DATA

Please replace paragraph [0001] with the following amended paragraph:

[0001] This is a continuation of copending U.S. patent application number 09/827,768 filed on April 7, 2001, now U.S. Patent 6,678,811 issued January 13, 2004, which is hereby incorporated by reference herein.